

# ADVANCED PACKAGING FOR GOVERNMENT NEEDS

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## A CHANGING LANDSCAPE REQUIRES MODERNIZATION

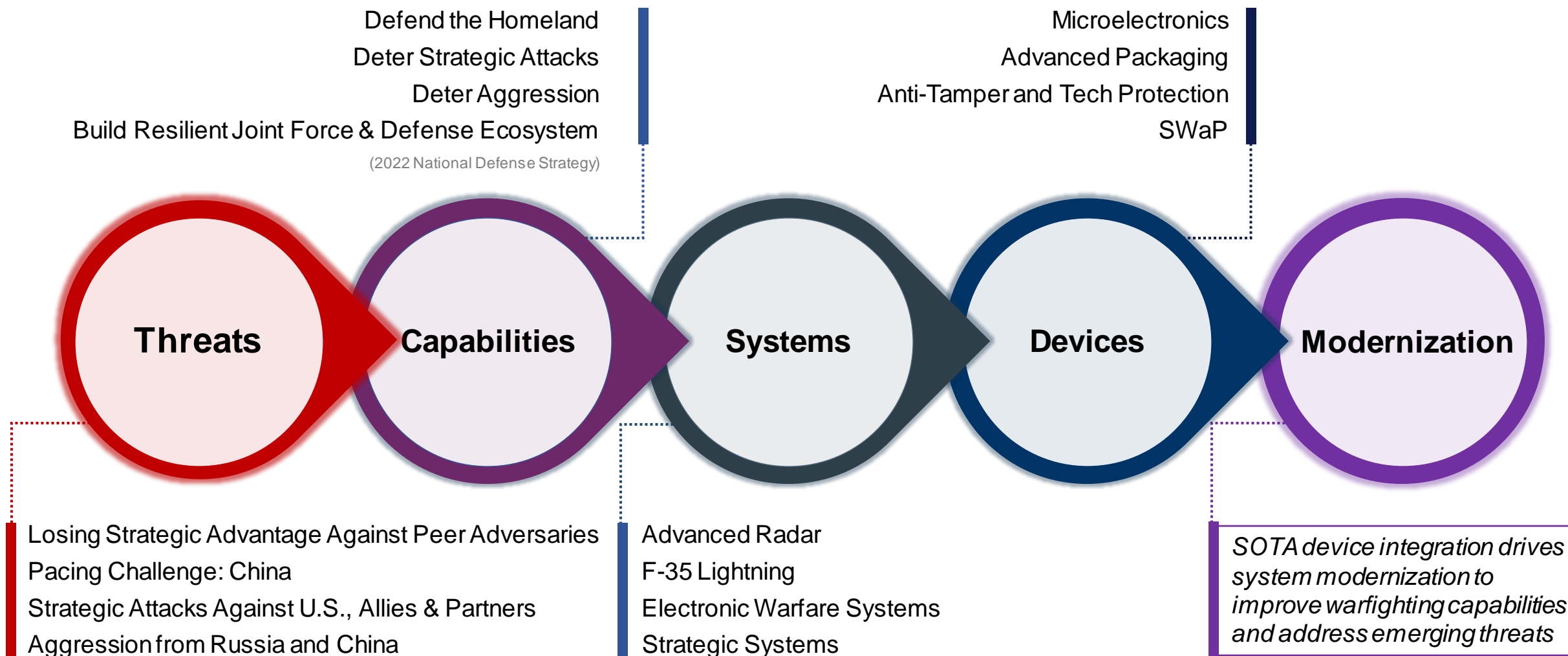
“ The strategic landscape is rapidly changing. We are witnessing a fundamental shift in the character of war. [...] China is increasing its military capability at an aggressive rate. We must ensure that we retain our competitive and technological edge against this pacing [challenge]. ”

**VADM Ronald A. Boxall, USN**  
Joint Staff Director of Force Structure,  
Resources & Assessment

Source: U.S. Marine Corps



# THREATS DRIVE WARFIGHTER NEEDS




# COMMERCIAL APPROACH HAS LOW SUSTAINMENT COST AND FASTER DEPLOYMENT

\*\* Normalized to 2022 dollars

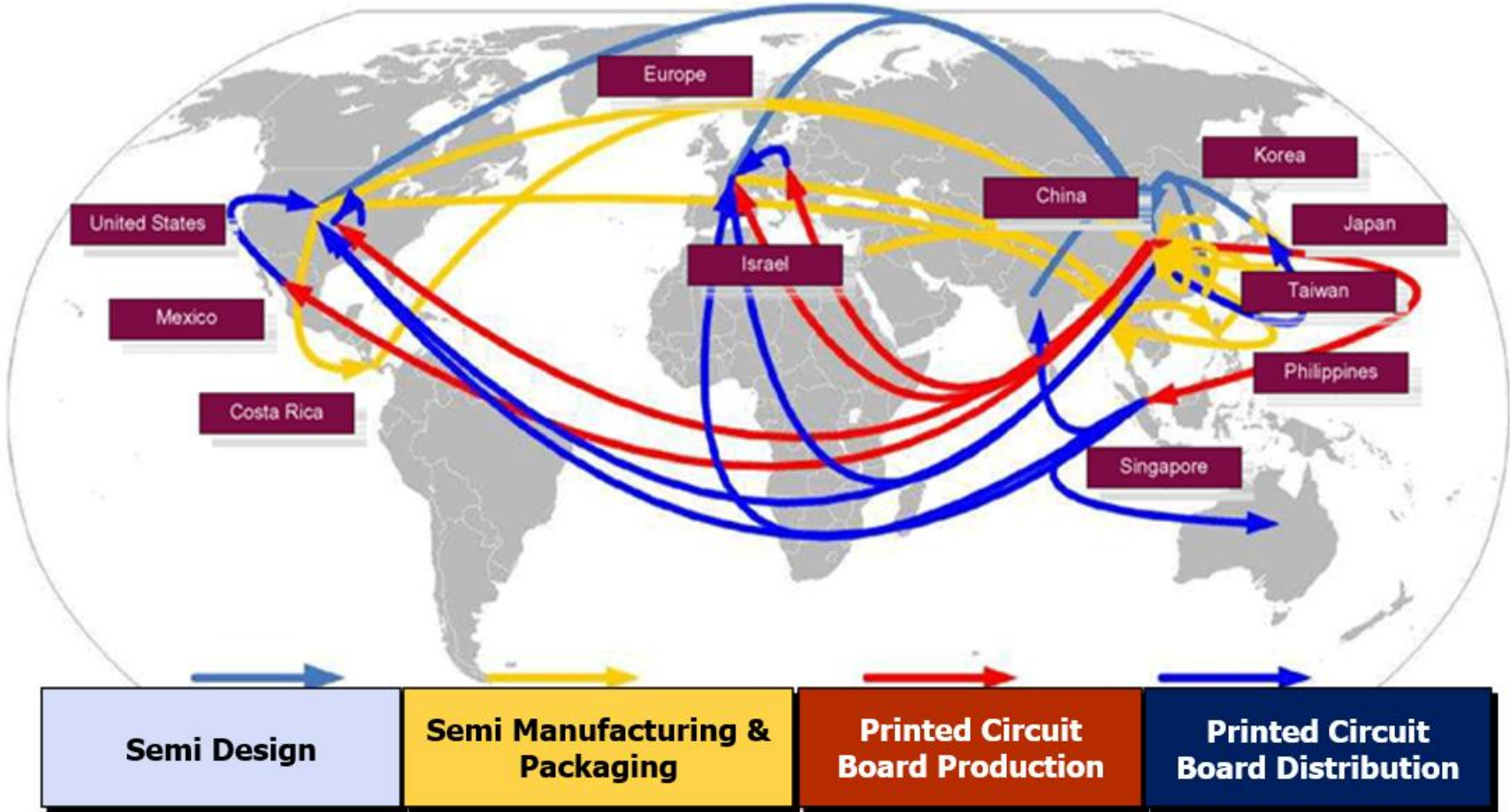
<p>2007 Touchscreen, apps, camera BOM \$307 **</p>  <p>BlackBerry</p>	<p>2010 + Higher-res, gyroscope BOM \$251 **</p>  <p>Samsung BlackBerry</p>	<p>2015 + Carplay, haptic I/F BOM \$261 **</p>  <p>Samsung Google</p>	<p>2017 + OLED, Face ID BOM \$442 **</p>  <p>Samsung Google</p>	<p>2020 + quad camera, XDR OLED BOM \$459 **</p>  <p>Samsung Google</p>
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- Multi-decade platform
- Evolving adversaries and adversary capabilities
- Revisions utilize new HW and methodologies
- BOM doesn't always increase for new HW

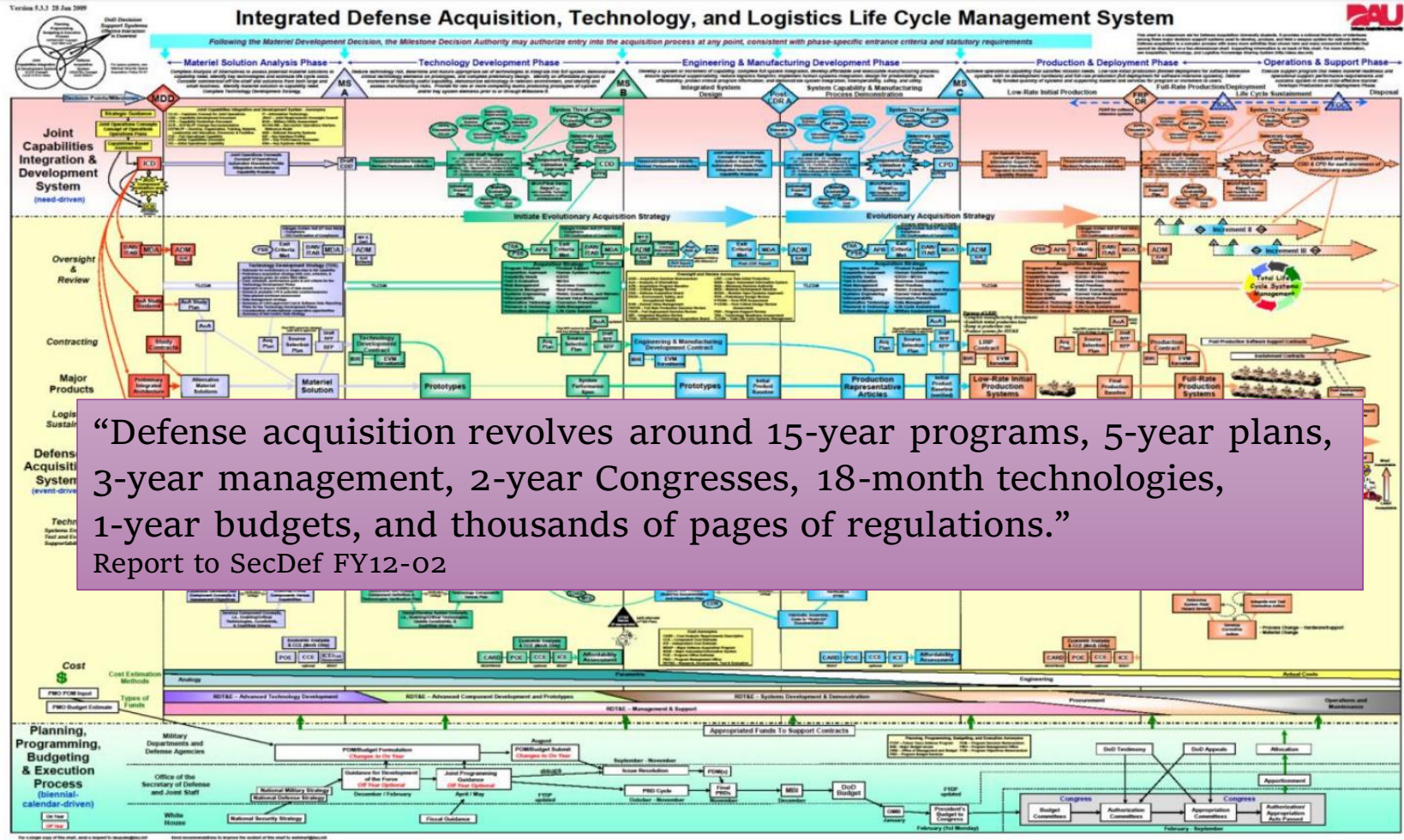
<p>2006 First flight</p>  <p>Russia China</p>	<p>2012-2017 operational life</p> 
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- 2023-2025
- Multi-decade platform
  - Evolving adversaries and adversary capabilities
  - Revisions focus on socket replacements
  - BOM increases for new HW
- HW system largely unchanged
    - DMS upcoming for multiple components
  - Operational life issues remain (assumed)
- Russia  
 Other ME suppliers  
 China
- } Adversaries Connecting

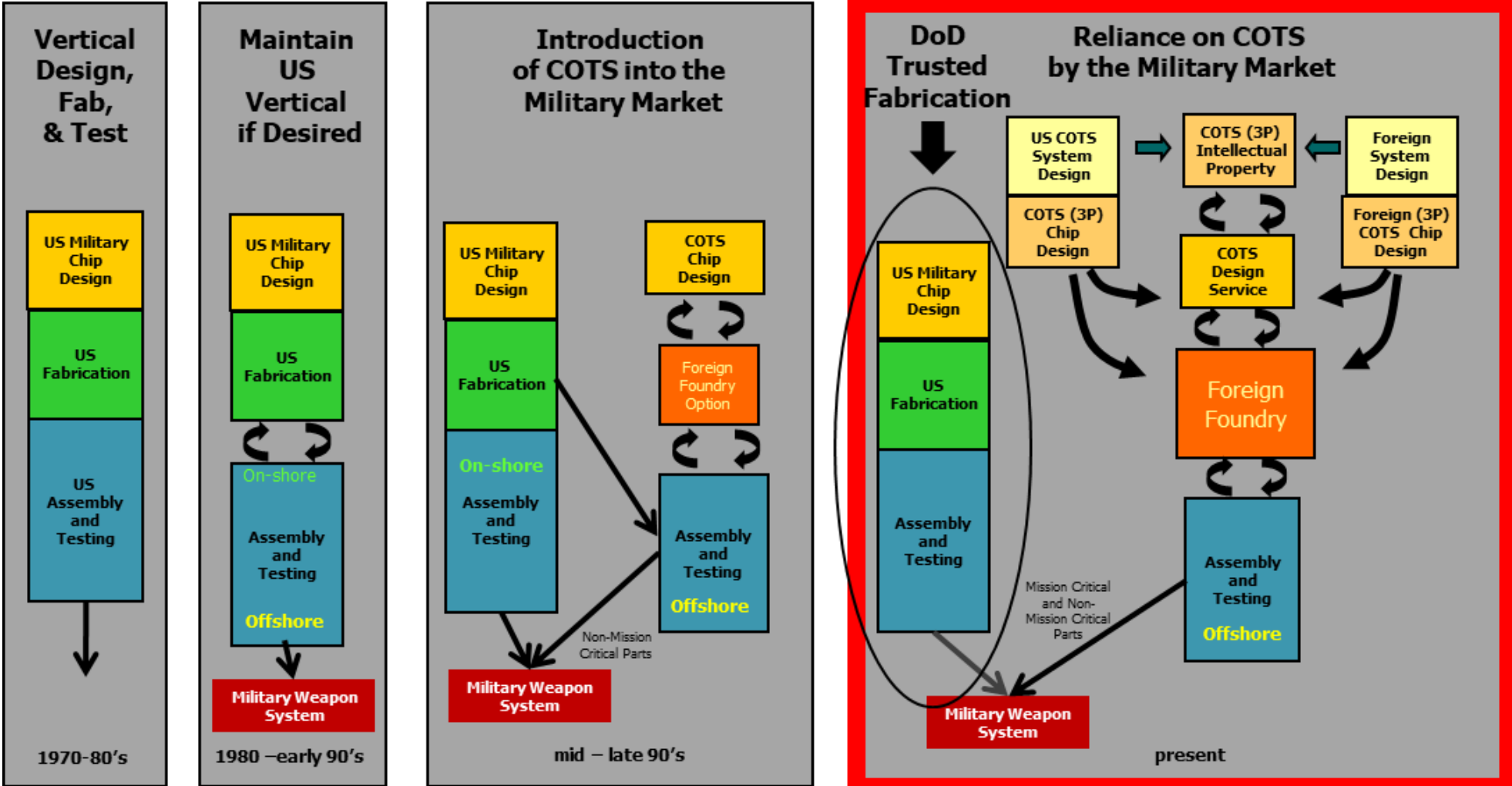
# SEMICONDUCTOR SUPPLY CHAIN – COMPLEX, GLOBAL SUPPLY NETWORKS



# DOD ACQUISITION IS A MAN-MADE CHALLENGE



# SUPPLY CHAIN EVOLUTION FOR MILITARY IC NEEDS



# HARDWARE SUPPLY CHAIN: THREATS HAVE EVOLVED AND BROADENED IN SCOPE

**Bloomberg Businessweek**  
October 6, 2018

## The Big Hack

How China used a tiny chip to infiltrate America's top companies

1 A Chinese military unit designed and manufactured microchips as small as a sharpened pencil tip. Some of the chips were built to look like signal conditioning couplers, and they incorporated memory, networking capability, and sufficient processing power for an attack.

2 The microchips were inserted at Chinese factories that supplied Supermicro, one of the world's biggest sellers of server motherboards.

*Our Take:*

- Threat actors have capability to embed chips in myriad systems
- Whether malicious or inadvertent, we need to ensure systems are protected from this kind of exploit

The image shows a close-up of a fingertip with a tiny microchip resting on it. An arrow points from the chip to a server motherboard. Below, a full view of the server motherboard is shown with the chip inserted into a component slot.

<https://www.bloomberg.com/news/features/2018-10-04/the-big-hack-how-china-used-a-tiny-chip-to-infiltrate-america-s-top-companies>



# ON-SHORING: FOUNDRIES IN THE U.S., BUT IS IT ENOUGH?



### Advanced Foundry Plans:

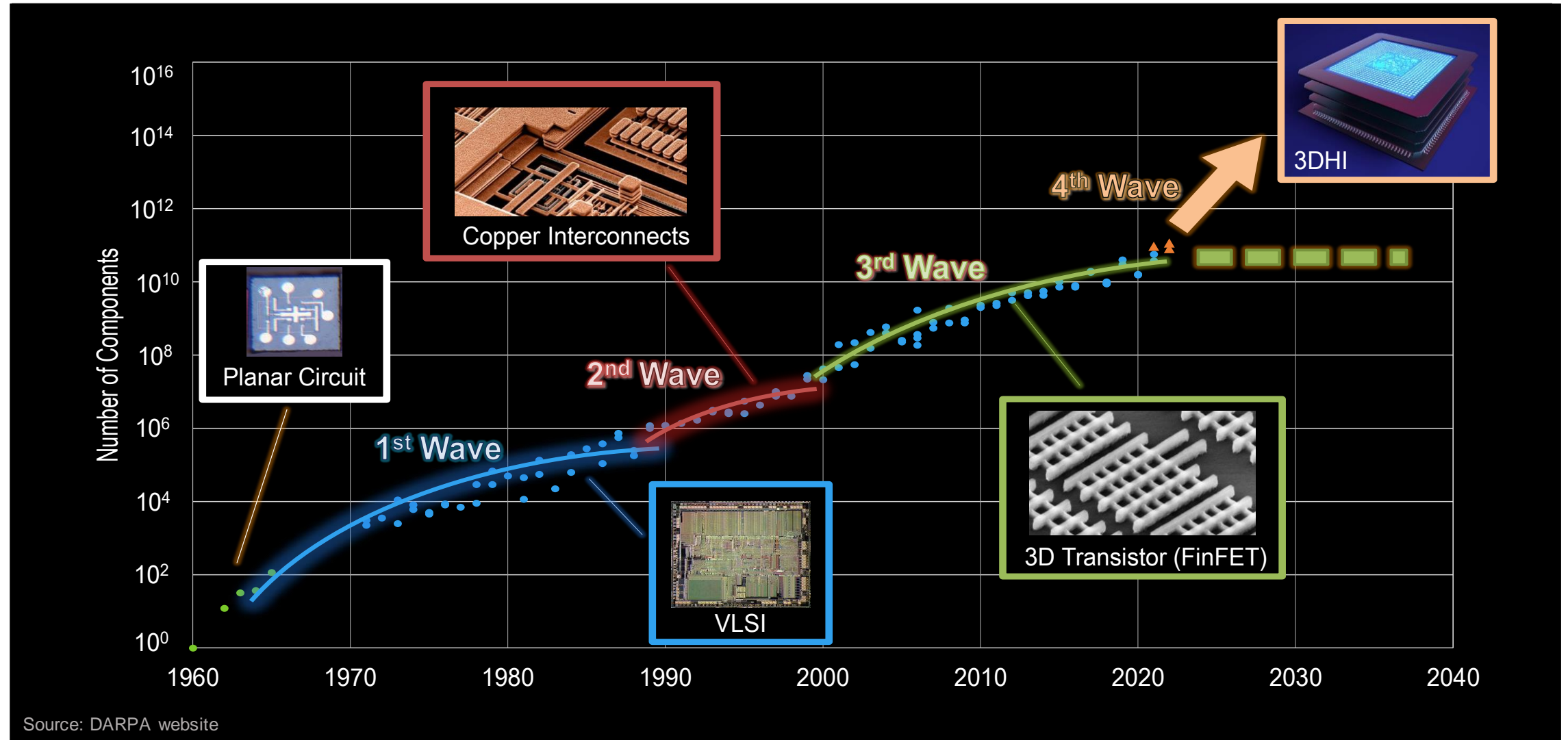
TSMC – 3nm in Phoenix, AZ

Samsung – 3nm in Taylor, TX

Intel – 7nm in Chandler, AZ

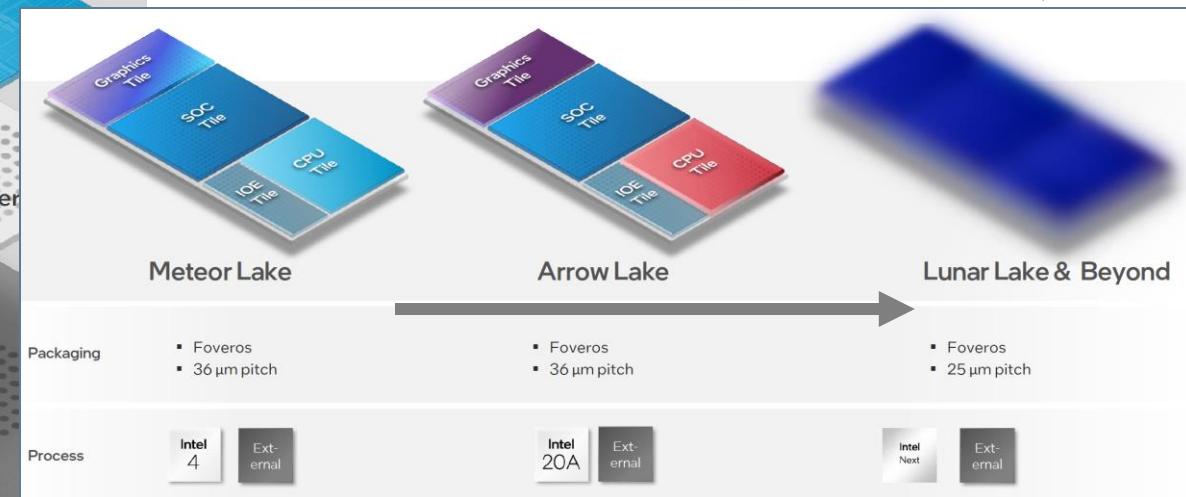
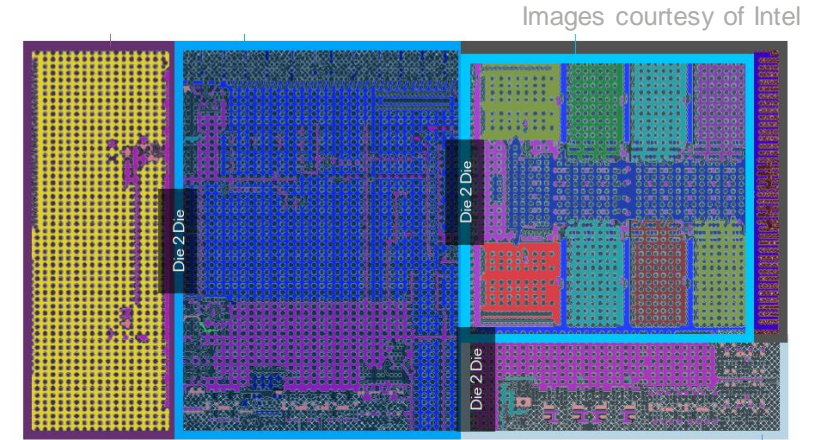
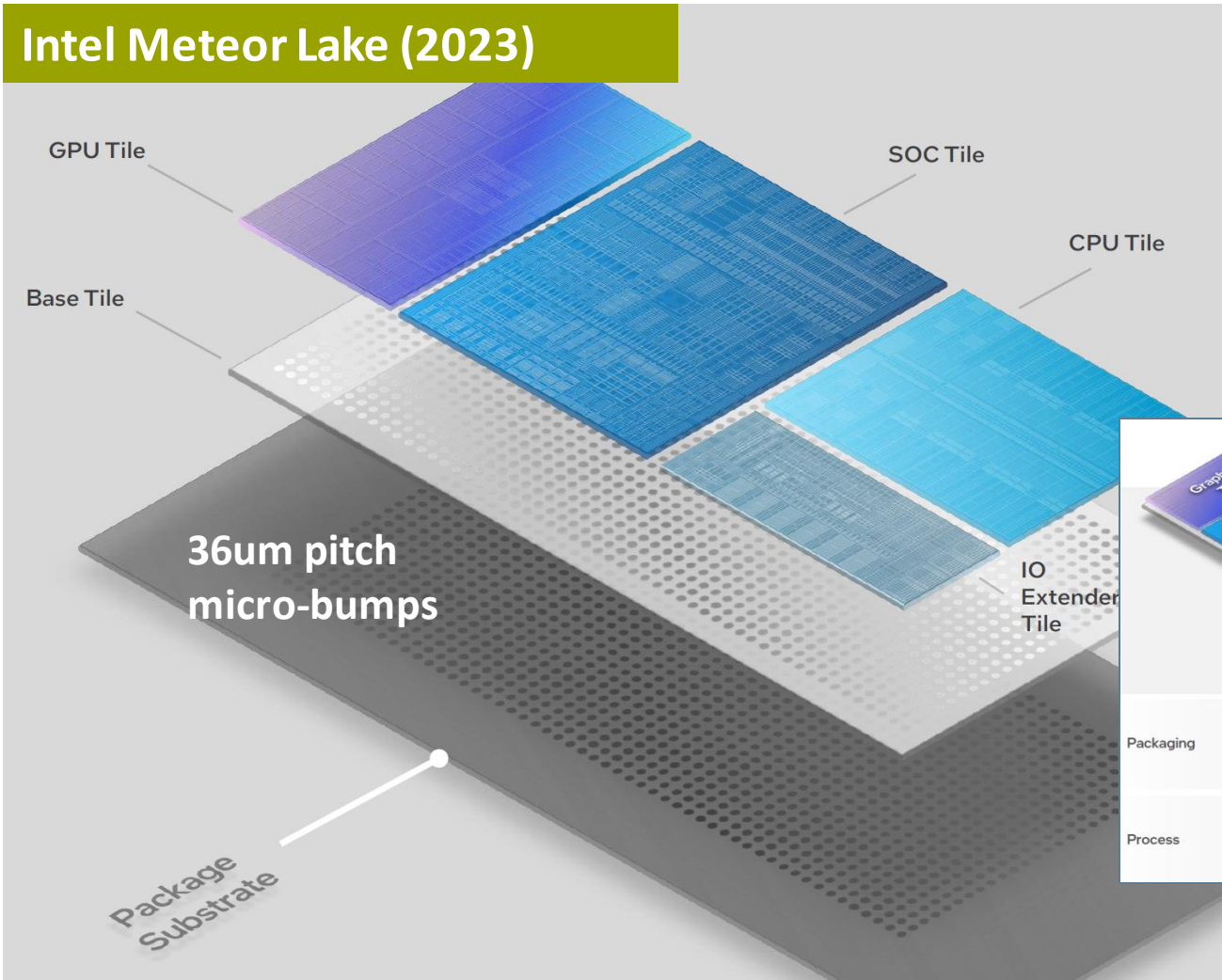
Source: SIA website

# DARPA ERI 2.0: FOUR WAVES IN MICROELECTRONICS INNOVATIONS



# THE FOURTH WAVE IS UNDERWAY

## Intel Meteor Lake (2023)



“This is our future.” B. Brennan

# OVERSEAS COMPETITION RIDING THE WAVE



## China forms its own chiplet standard amid isolation

Technology News | March 27, 2023

By Peter Clarke

INTERCONNECT & CABLES

CHINA

IOT

ANALOG

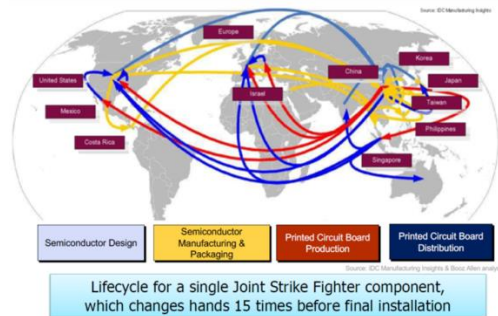
“ This could allow China’s trailing-edge manufacturing capability to continue to contribute to relatively advanced ASICs. ”

The China Chiplet Industry Alliance has released the ‘Chiplet Interconnection Interface Standard’ known as ACC1.0, according to the *Financial Association Press*.

<https://www.eenewseurope.com/en/china-forms-its-own-chiplet-standard-amid-isolation/>

# CHALLENGES FOR THE GOVERNMENT IN THE ADVANCED PACKAGING (AP) LANDSCAPE

## Complex International Supply Chain



- SOTA microelectronics devices travel around the globe many times over
- >90% of packaging activities are done offshore
- Global semiconductor manufacturing is at 100% capacity

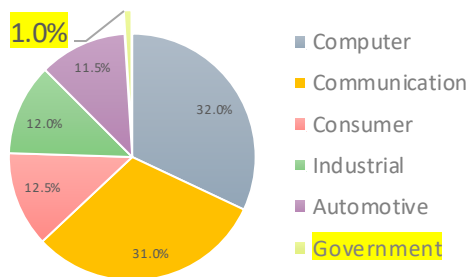
## Heterogeneous Integration Challenges



- New/emerging commercial trends, chiplets and HI, provide a unique opportunity for USG and DoD to gain SOTA packaging solutions
- USG and Industry collaboration is critical to address challenges

## Consumer-Driven Market

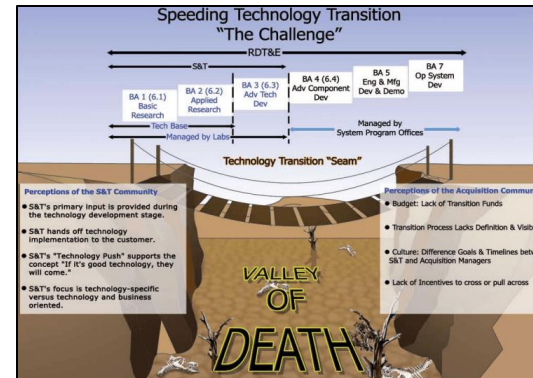
SEMICONDUCTOR MARKET SEGMENTS: \$440.4 BILLION



Source: SIA 2021

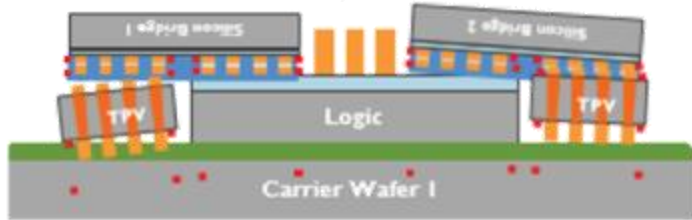
- DoD is <1% of total microelectronics revenue
- Market and innovation is driven by high-performance commercial market
- High-mix, low-volume DoD needs don't align with commercial strategy

## Rapid Technology Change and Adoption



- While front-end nodes advance every 2-years, AP technologies advance much more rapidly
- Lifecycle of DoD systems leads to need for consistent modernization
- "Valley of Death"

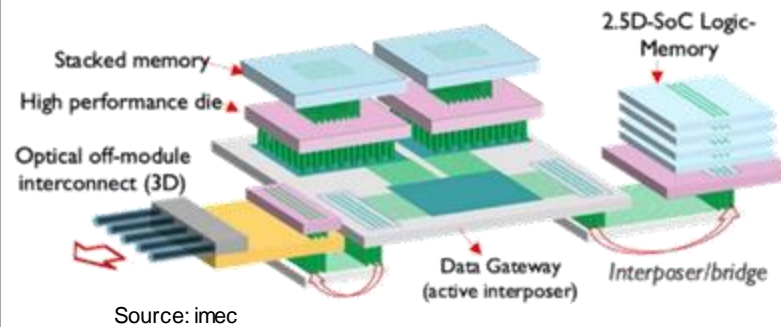
# ADDRESSING CHALLENGES: DOD AND INDUSTRY COLLABORATION



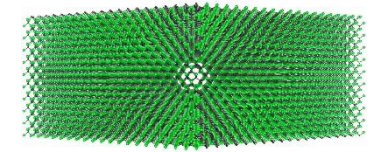
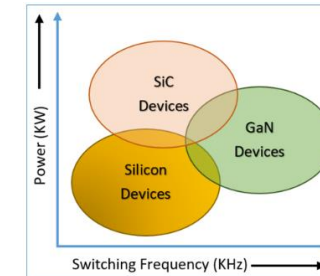
Source: imec

WLP combined with 2.5D/3D lowers latency and can reduce power consumption  
**Warpage, Die Shift, Die Tilt, Reliability**

**DoD and Industry collaboration will enable solutions for SOTA packaging challenges while granting the DoD access to SOTA packaging through programs like SHIP**

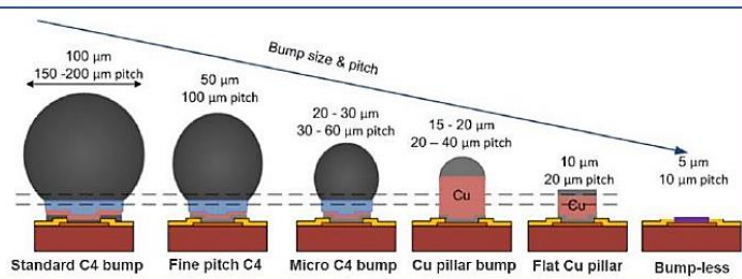


Source: imec



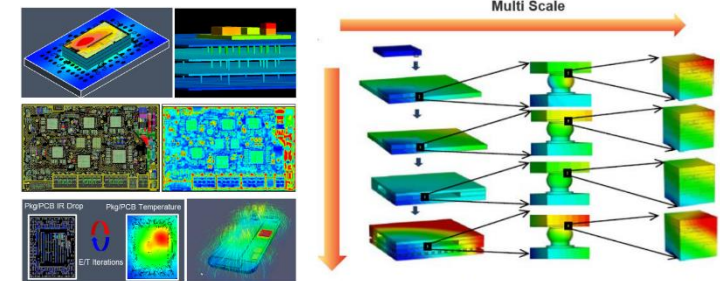
Source: AIP Publishing

Integrating material sets for HI to support high power and mixed signals  
**Thermal Management, CTE Mismatch**



S. Ramalingam, CMC conf. 2016

Bump scaling leads to higher interconnect density for increased performance  
**Yield, Reliability, Domestic Capability, Capital Investment**



Source: Cadence

Multi-process and Multi-scale CPI simulation flow [Source: R. Rao, IRPS, 2014]

Design & verification for HI requires multi scale and multi physic tools  
**Cost, Design Time**

# ADDRESSING A CONSUMER-DRIVEN MARKET: THE NAVY SHIP PROGRAM

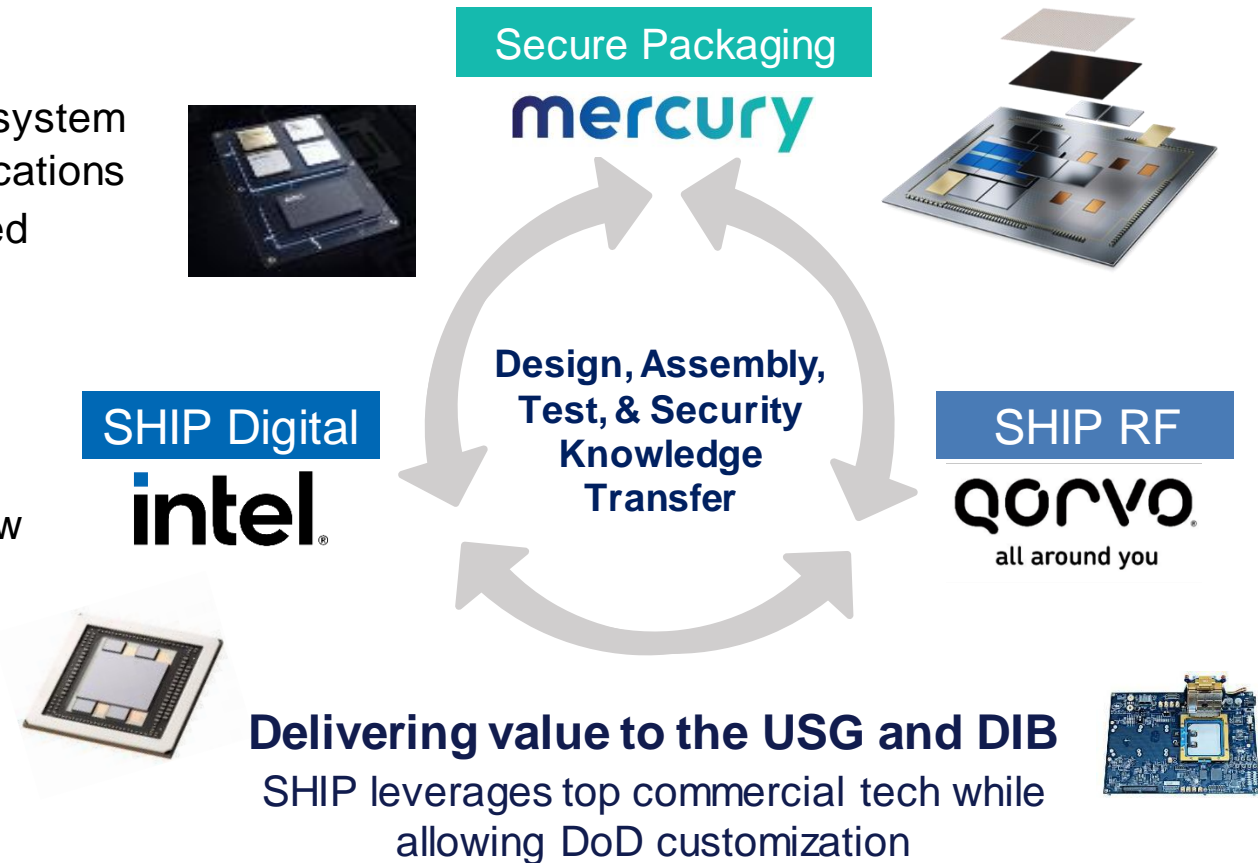
## State-of-the-Art Heterogeneous Integrated Packaging (SHIP) Program

### Motivation

- Earlier access to assured SOTA microelectronics
- US-based, economically-viable SHIP capability for DoD system performance enhancement and assurance/security applications
- Provide sustainable, quantifiably-assured SOTA advanced packaging access to the DoD and the DIB

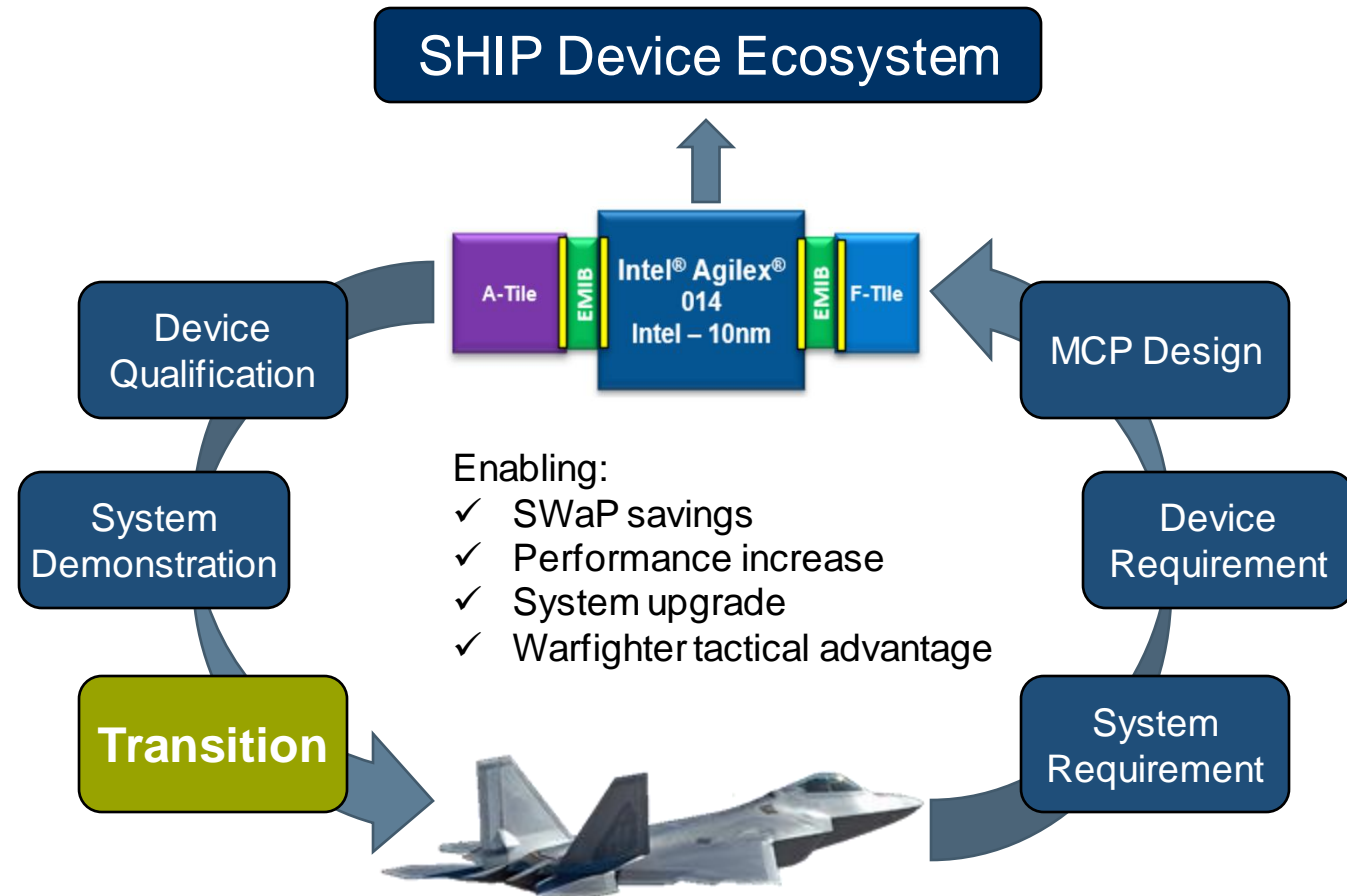
### Objectives

- Develop a model for access to SOTA parts
  - Self-sustained business model for DoD access to customized SOTA parts using standard commercial flow
  - Prototypes validate the model, are not the end goal
- Advance DoD capabilities
  - Improved performance, SWaP-C
  - Availability of SOTA parts
  - Domestic manufacturing
  - Increased functional density
  - Pathway to modernization



# ADDRESSING RAPID TECHNOLOGY CHANGE: SHIP TRANSITION

- **DIB, Services, and Program Office collaboration is critical for SHIP device transition**
- DIB is in the position to translate DoD warfighting capabilities down to MCP functionality and performance requirements
- SHIP is a prototyping program; transition falls on the Acquisition community
- Navy is engaged in transition strategy extended past typical prototyping efforts
- Transition is a critical metric of success; must be clearly demonstrated to sustain future programs





# DOD MODERNIZATION IMPACT

## System Capability Modernization

- Increased Functional Density and Performance
- Facilitated through SWaP-C improvements
- Enables increased functional density of systems; capabilities and performance continually augmented and fit into the same form factor



F35A 2006 1<sup>st</sup> Flight

Continuous enhancements and improvements will be made to increase capabilities that **make the F-35 more lethal and survivable.**



Increasing the number of separate subsystems into ever more tightly integrated packages → Increased function, performance, capabilities



Current Block 4



**F-35 Lightning II**

**Basic Missions:**

- Air superiority
- Close air support
- Destruction of air defenses
- Strategic attack

**New in Block 4:**

- Extended surface warfare

**Block 4 upgrades include new or improved capabilities:**

- 13** Electronic warfare updates
- 11** Radar and electro-optical system enhancements
- 8** Logistic and support changes
- 7** Interoperability and networking changes
- 7** Cockpit and navigation upgrades
- 7** New weapons systems

**Some Block 4 Weapons Upgrades**

Source: Joint Strike Fighter Program; Boeing, NGA, Raytheon, Lockheed Martin

Weapons: Joint Strike Fighter (JSF), JSF (Block 4), JSF (Block 4), JSF (Block 4), JSF (Block 4)

53 improvements to counter both air- and ground-based threats

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THANK YOU!