

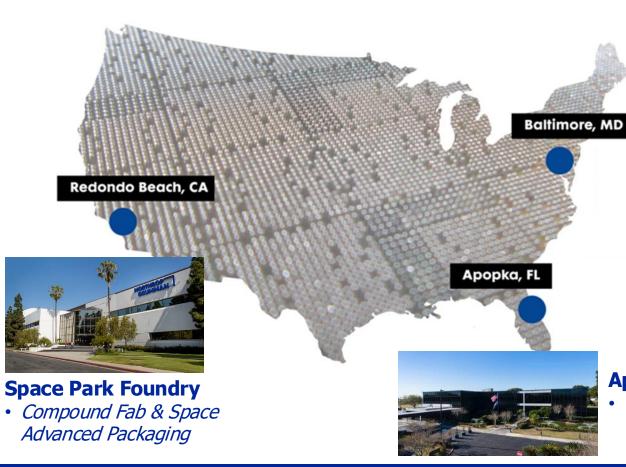
Louise Sengupta
Director
Northrop Grumman
Microelectronics Center



Northrop Grumman Microelectronics Center (NGMC)

Effective in 2024, NG is Providing Open Access to its Microelectronics Capabilities

- Integrated Multifunction Sensing
- Quantum Science
- Future-G Wireless Technology
- Advanced Materials
- Space & Rad Hard Technologies
- Renewable Energy
 Generation and Storage
- Advanced Computing
- Human-Machine Interfaces
- Directed Energy
- Hypersonics





Advanced Technology Laboratory

- Silicon, Compound, & Specialty Device Fab
- Advanced Testing
- Wafer Post-Processing
- 2D, 2.5D & 3D Advanced Packaging

Apopka µ-Line

 Wafer Post-Processing (Bump, Dice, Test)

NGMC Capabilities and Strategic Partnerships Provide Flexible, Assured Microelectronics for Commercial and Defense Industrial Base Companies



Trends We See Within the Domestic Microelectronics Base and the Northrop Grumman Microelectronic Center Solution

Mil / Aero Needs

- High Mix / Low Volume over many years
- Performance, SWaP, reliability driven
- Secured / Trusted / Assured sourcing

Commercial Supplier Gaps

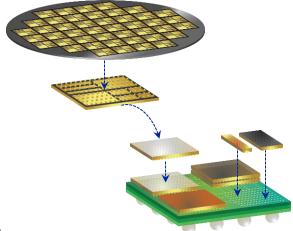
- High-volume low-mix focus
- International / ITAR
- Rapid product change

Overarching Gaps

- Technical knowledge base
- IP protection
- Secure & Trusted supply
- Rapid DMS vs. long-term stability

Domestic Supplier Gaps

- Limited capability / capacity
- Investment business-case
- Fed-Ex Flow, no 1-stop shop



NG's Microelectronics Development, Solutions and Products Are Based on USG System Needs

NGMC Offers Our Assured And Long-Term Access To USG Microelectronics Technologies

NGMC Access Could Be For a Single Service(s) or End-to-End Microelectronics Solutions

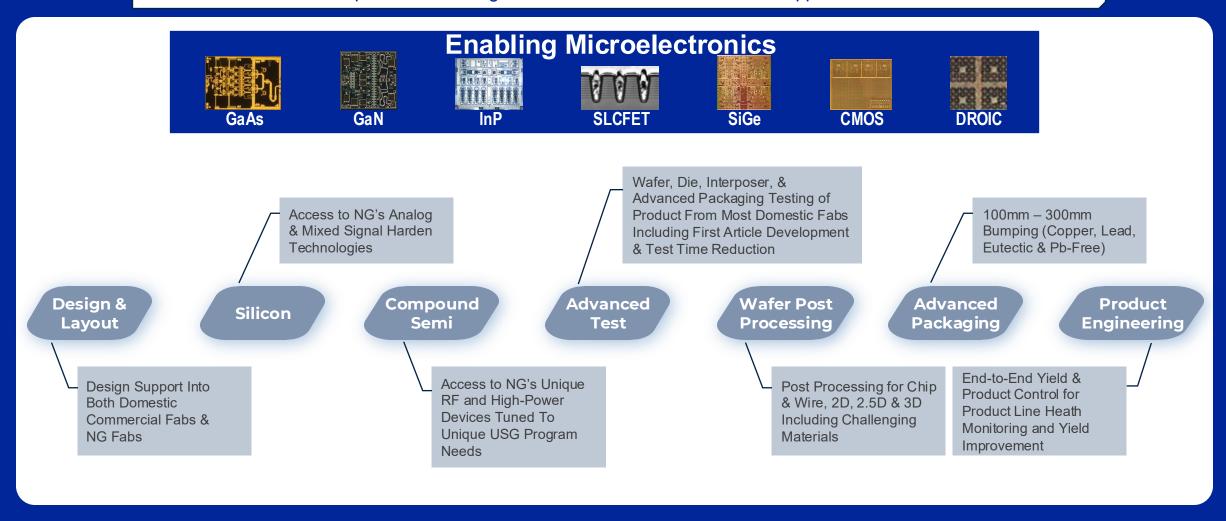
NGMC Access Includes Listed Products, Tape Out Unique Designs,
New Product Development, Testing, Post-Processing And Product Engineering



NGMC Open Access Offerings

Northrop Grumman Fills the Microelectronics Gaps Across the Domestic Industry

NG Has Extensive Experience Working With A Mix of NG & Commercial Suppliers As Part Of A Value Chain





NGMC Open Access Offerings

Northrop Grumman Fills the Microelectronics Gaps Across the Domestic Industry

Design & Layout	Silicon	Compound Semi	Advanced Test	Wafer Post Processing	Advanced Packaging	Product Engineering
Aggression (MPW) & Layout Services	10V, 15V, 40V & 60V Radiation Harden	0.15 & 0.1um GaAs HEMT	6", 8" & 12" Wafer & Die-Level Digital	Mechanical & Laser Wafer Dicing	100mm – 300mm Lead, Eutectic &	New Product Development
NGMC's Fabs	BiCMOS	1.0um GaAs HBT	ASIC (180nm – 5nm) Testing	Mechanical Wafer	Pb-Free Solder Sphere Drop Based Wafer	IP Protection Controls
GlobalFoundries™	0.5um & 0.8um Radiation Harden	0.2, 0.15 & 0.09um PWR GaN HEMT	6", 8" & 12" RFIC Wafer & Die-Level	& Die Grinding	Bumping	Post Test Analysis
Gleball Callanes	CMOS w/ Volatile Memory (NVM)	0.15um LNA GaN	Testing	Wire Bonding	100mm – 300mm Copper Plated	Screening
Intel™	Radiation Harden EEPROMs	O.lum InP HEMT	Bare Pad & Bumped Film Frame Wafer,	Environmental Testing: HAST, Leak, Centrifuge,	Bumps and Pillars Bumping	Product Line Heath Monitoring
TowerJazz TM	L-Band & S-Band	70, 35 & 25nm InP	Singulated Wafer & Die-Level	RF Shield Die Marking & Traceability Gel, Waffle and	Under Bump Metallization	Yield & Quality Management (including through FedEx Flow)
Texas Instruments™	Bipolar Power SiGe Transistors	IACC	Testing		(UBM)	
	Charge Coupled	0.8 & 0.65um InP TF	Interposer (Wafer, Glass & Organic)		2D & 3D Automatic Optical	
Qorvo™	Devices (CCD)	SiC 100V Schottky	Testing	Tape & Reel	Inspection (AOI)	Qualification Plan
Wolfspeed™	Superconducting	SIT Power Trans.	2D, 2.5D & 3D Package Testing		2D, 2.5D & 3D Die – Wafer Assembly	Development
14/100 ATM	Electronics (SCE)	-20 & -14V SLCFET Switches & Diodes	Classified (SCI &		Chip Scale	Document Configuration
M/ACOM TM		2.5 – 15V SLCFET	SAP) Options		Hermetic Packaging	Product
Skywater™		Amplifiers (LNAs & HPAs)	Reliability & Environmental			Disposition
Skywater TM		Amplifiers (LNAs &	3		Packaging	



NGMC Open Access Offerings – Example Usage of NGMC

To Address a Low-Band Radar

Design & Layout	Silicon	Compound Semi	Advanced Test	Wafer Post Processing	Advanced Packaging	Product Engineering
Aggression (MPW) & Layout Services	10V, 15V, 40V & 60V Radiation Harden BiCMOS	0.15 & 0.1um GaAs HEMT	6", 8" & 12" Wafer & Die-Level Digital ASIC (180nm –	Mechanical & Laser Wafer Dicing	100mm – 300mm Lead, Eutectic & Pb-Free Solder	New Product Development
NGMC's Fabs	0.5um & 0.8um Radiation Harden	1.0um GaAs HBT 0.2, 0.15 & 0.09um PWR GaN HEMT	5nm) Testing 6", 8" & 12" RFIC	Mechanical Wafer & Die Grinding	Sphere Drop Based Wafer Bumping	IP Protection Controls Post Test Analysis
GlobalFoundries [™] Intel [™]	CMOS w/ Volatile Memory (NVM)	0.15um LNA GaN HEMT	Wafer & Die-Level Testing Bare Pad &	Wire Bonding Environmental	100mm – 300mm Copper Plated	Screening Product Line
TowerJazz™	Radiation Harden EEPROMs	0.1um InP HEMT 70, 35 & 25nm InP	Bumped Film Frame Wafer, Singulated Wafer	Testing: HAST, Leak, Centrifuge, RF Shield	Bumps and Pillars Bumping Under Bump	Heath Monitoring Yield & Quality
Texas Instruments™	L-Band & S-Band Bipolar Power SiGe Transistors	0.8 & 0.65um InP TF	& Die-Level Testing	Die Marking & Traceability	Metallization (UBM) 2D & 3D	Management (including through FedEx Flow)
Qorvo TM	Charge Coupled Devices (CCD)	SiC 100V Schottky	Interposer (Wafer, Glass & Organic) Testing	Gel, Waffle and Tape & Reel	Automatic Optical Inspection (AOI)	Qualification Plan Development
Wolfspeed™	Superconducting Electronics (SCE)	SIT Power Trans. -20 & -14V SLCFET	2D, 2.5D & 3D Package Testing		2D, 2.5D & 3D Die – Wafer Assembly Chip Scale	Document Configuration
M/ACOM TM		Switches & Diodes 2.5 – 15V SLCFET Amplifiers (LNAs &	Classified (SCI & SAP) Options Reliability &		Hermetic Packaging	Product Disposition
Skywater™		HPAs)	Environmental Testing			



NGMC Open Access Offerings – Example Usage of NGMC

To Address a Space Payload

Design & Layout	Silicon	Compound Semi	Advanced Test	Wafer Post Processing	Advanced Packaging	Product Engineering
Aggression (MPW) & Layout Services NGMC's Fabs	10V, 15V, 40V & 60V Radiation Harden BiCMOS	0.15 & 0.1um GaAs HEMT 1.0um GaAs HBT	6", 8" & 12" Wafer & Die-Level Digital ASIC (180nm – 5nm) Testing	Mechanical & Laser Wafer Dicing Mechanical Wafer	100mm – 300mm Lead, Eutectic & Pb-Free Solder Sphere Drop Based Wafer	New Product Development IP Protection Controls
GlobalFoundries™	0.5um & 0.8um Radiation Harden CMOS w/ Volatile Memory (NVM)	0.2, 0.15 & 0.09um PWR GaN HEMT 0.15um LNA GaN HEMT	6", 8" & 12" RFIC Wafer & Die-Level Testing	& Die Grinding Wire Bonding	Based Waler Bumping 100mm – 300mm Copper Plated	Post Test Analysis Screening
Intel TM TowerJazz TM	Radiation Harden EEPROMs L-Band & S-Band	0.1um InP HEMT 70, 35 & 25nm InP	Bumped Film Frame Wafer, Singulated Wafer & Die-Level	Environmental Testing: HAST, Leak, Centrifuge, RF Shield	Bumps and Pillars Bumping Under Bump Metallization	Heath Monitoring Yield & Quality Management
Texas Instruments TM Oorvo TM	Bipolar Power SiGe Transistors Charge Coupled	0.8 & 0.65um InP TF	Testing Interposer (Wafer, Glass & Organic)	Die Marking & Traceability Gel, Waffle and Tape & Reel	(UBM) 2D & 3D Automatic Optical	(including through FedEx Flow)
Wolfspeed™	Devices (CCD) Superconducting Electronics (SCE)	SiC 100V Schottky SIT Power Trans. -20 & -14V SLCFET	Testing 2D, 2.5D & 3D Package Testing	таре а кеег	Inspection (AOI) 2D, 2.5D & 3D Die – Wafer Assembly	Qualification Plan Development Document Configuration
M/ACOM TM Skywater TM		Switches & Diodes 2.5 – 15V SLCFET Amplifiers (LNAs &	Classified (SCI & SAP) Options Reliability & Environmental		Chip Scale Hermetic Packaging	Product Disposition
		HPAs)	Testing			

Key NGMC Technology Capabilities

Driven By Over 2,000 Microelectronics Experts

ATL Production Technologies

Supporting >70 RF Systems Supporting Space, Air, Cyber, Maritime and Ground Systems

- •100 mm GaAs InGaP HBT, pHEMT
- •100 mm GaN SLCFET Switch & Filters
- •150 mm Silicon
 - CMOS* (180nm, 0.5µm, 0.8µm, & 1.25µm)
 - BiCMOS (10V*, 15V, 40V*, 60V)
 - SONOS Non-Volatile Memory*
 - SiGe Bipolar Power
 - Imaging ITO CCD
- •100 mm SiC Schottky SIT and Ion Implanted SIT
- *Radiation Hardened options available

Emerging Technologies Supporting Next Gen System Insertions

• Superconducting RQL





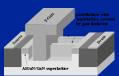
- Diamond Electronics
- Acoustic Filters
- Strained Layer Superlattice Detectors

External Foundry Test

Supporting Advanced Testing

- Multi-Chip Interposer Module Testing (IPA)
- Bumped Die / Film Frame Testing
- RFIC & ASIC Wafer Testing







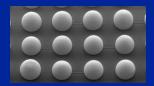


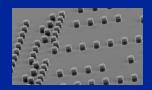
Advanced Packaging Technologies

Wafer Post-Processing (Apopka)

Supports Mission & Space Systems

- Nation's only domestic one-stop shop for bump/dice/test
- 100, 200, 300 mm wafers Si, SiGe, GaN, etc.
- Lead and lead-free solder bumping + Cu-pillars
- Copper pillars for 100 mm
- Mechanical wafer dicing & thinning
- 200/300mm wafer test extension of ATL for production
- Domestic leader in 3D automated wafer inspection
- 200/300mm automation & capacity expansion in progress





2/3D Assembly (Baltimore)

Advanced prototyping to production for NG package assemblies across RF & EO/IR domains

- Dedicated AP Cell within the new NG SALT facility
- R&D to production for advanced flip-chip assembly
- Pick/place/reflow + high accuracy thermocompression bond
- Advanced underfill
- High-throughput BGA attach
- Laser depanel for organic substrates







Space Park Production Technologies

Supporting Space, Air, Maritime and Commercial Programs. All technologies are Rad Hard

- GaAs HBT, HEMT
- InP HBT (TFx)
- InP HEMT (N60D/E, IACC70, IACC35/25)
- GaN Power (0.2um, 0.15um, 90nm, 50nm)
- GaN High Survivability LNA (0.15um)
- SAW Filters
- Substrates (Alumina, Quartz, BeO)
- · Limiters (GaAs+, GaN, Plasma)
- NLTL (Harmonic Generation to 100 GHz)

Advanced Packaging

- Wafer Level Packaging (GaAs*, GaN*, InP)
- Diverse Accessible Heterogeneous Integration (DAHI) for Passive, Mixed Signal, Photonic and Digital Applications
- SiC Interposers
- Reliability without Hermeticity (RWOH)
- Chiplet Processing
- * 2-level qualified for Space





Emerging Technologies

Supporting Next Gen System Insertions

- Ultra Wide Band Gap Semiconductors (e.g. AIN)
- Space Power Conversion Electronics
- LINRFET (Low loss switches and high-linearity amplifiers)
- Compact Tunable Filters
- Transferred Epitaxial Layers
- Microelectronics-based High Power Lasers



NORTHROP— GRUMMAN

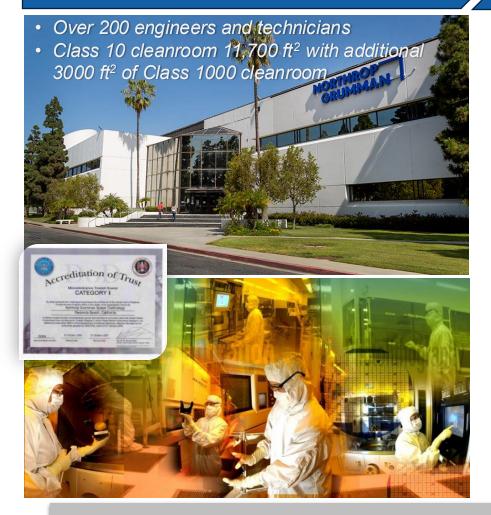




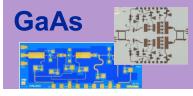


Space Park Foundry

Trusted Microelectronics



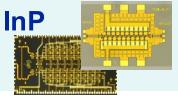
Discriminating Capabilities



 RF, microwave, mixed signal, digital



- High power SSPAs
- Survivable LNAs



- Wide band LNAs/PAs
- SC Modulators
- Frequency sources

Wafer Level Packaging



- All RF products
- Wideband Phased arrays

DAHI

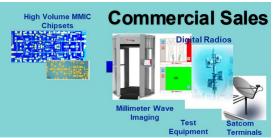


- Wideband flexible systems
- System on a Chip

Mission Enabling Performance







Space Park is a leader in III-V semiconductor development and manufacturing



NGMC Microelectronics Technologies – Space Park

			gios opassi	
** Space Qualified * Commercially Qu		Key Applications	Key Discriminators	Mission
	GaAs • 0.15 um HEMT TRL= 9** • 0.1 um HEMT TRL = 9** • 1.0 um HBT TRL = 9 **	RF, microwave, mixed signal, digital	Flight qualifiedMission ProvenTRL 9	• EW • Comms • ISR
	 GaN 0.2 um PWR HEMT TRL= 8* 0.15 um LNA HEMT TRL = 8** 0.15 um PWR HEMT TRL = 8* 0.09 um PWR HEMT TRL = 6 	High power SSPAsSurvivable LNAs	 High frequency, high PAE, wide bandwidth High reliability 	• EW/EA • Comms
	• 0.1 um HEMT TRL= 9** • 70, 35, 25 nm IACC TRL = 6 • 0.8 um TF2 TRL = 8 • 0.65 um TF4 TRL = 8	LNAsSC ModulatorsWB ADC, DAC	Lowest NoiseHighest FreqLowest Power (dB/mW)	 Electronic Support Array Rx
	Wafer Level Packaging GaAs/GaAs TRL= 8** GaN/SiC TRL = 8** InP/InP TRL = 7 GaAs/InP TRL = 7 SiC Interposer TRL = 4	 Air and Space All RF products Wideband Phased arrays	 Hermetic Micro-package Lowest size / weight / cost 	 Affordability Array Rx
10	 3DHI CMOS/InPTRL= 5 CMOS/InP/GaNTRL = 4 WLP/WLPTRL = 4 GaAs/GaAsTRL = 5 	Wideband flexible systemsSystem on a Chip	 Si, InP, GaN, on single chip NGAS is sole DAHI Foundry 	• Array Rx / Tx



NGMC Microelectronics Technologies – Baltimore



ATL Is Unique – No Other Facility In The United States Has As Large a Variety Of Semiconductor Technologies Under One Roof

High Mix / DoD-Volume Microelectronics

Super Conducting Electronics

GaAs HBT GaAs MESFET Silicon ASICS
Controllers
Regulators
CCDs
NVM EEPROM
Rad-Hard ASICs

SiC Power Si/SiGe Power

GaN SLCFET

Ultra-Wide Bandgap







Si, SiGe, SiC RF Power Transistors (VHF to S-Band)





























Si ASIC Controllers, Power Regulators/Fault Isolators,

Space Qualified EEPROMs,

A/D Converters, CCD Imagers





GaN SLCFET T/R switches, High performance switched Filters









NGMC Microelectronics Technologies – Baltimore

Taking Wafers, Die and Multi-Chip Modules from Outside Entities and Performing Advanced Testing

Multi-Chip Interposer Module Testing (aka: IPA)

- Automated handling capability (semi and fully auto) for high throughput
- Test development cost savings through integration of test SW and database from wafer to assembled device
- Test cost reduction with leveraging commercial ATE, RF & digital integrated test platforms
- Full End to End (RF to Bits) testing capability (development to start in 2023)

Bumped Die / Film Frame Testing

- Bumped Flip Chip, Diced Wafer on Film Frame probing
- 8"/12" Film Frame fully automated prober with self alignment
- Final AOI, Bake, Tape & Reel or Gelpak packaging





RFIC & ASIC Wafer Testing

- PNAX-based RF testing w/ integrated digital interface
- Commercial ATE for high I/O and clock speed
- 8"/12" wafer auto wafer inspection & probing







ATL Product Engineering Services

Production

Configuration



Process

Excursion

Management

•PSAF

Trip Ticket

 Supplier RCCA technical support (w/w Category Mgt & QME)

 Access to supplier defect analysis library

Other Engineering Services

 Qualification HW **Design & Testing** Cleaning, Dicing & Inspection Expertise











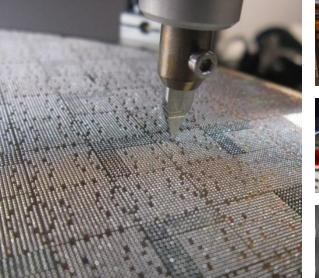
NGMC μ-Line for Wafer Post-Processing

Domestic Wafer Post-Processing Capabilities Accessible Onshore

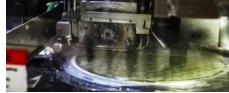


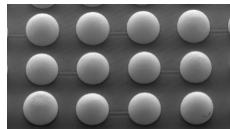




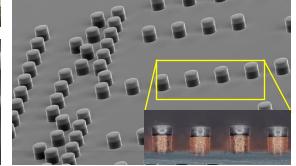










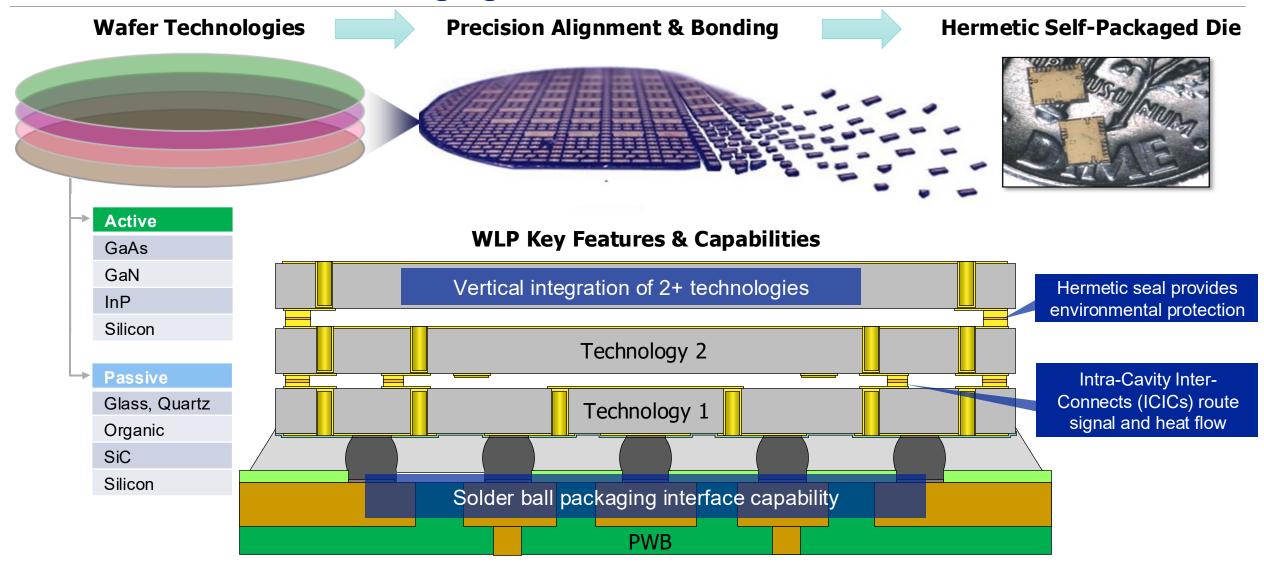


μ-Line is tailored to meet diverse post-processing needs in a flexible one-stop shop

- Sources wafers from both NG Fabs and external domestic commercial Fabs (GF, Tower, Skywater, etc.)
- 100-300 mm wafer backend processing – passivation, bump, dice, test, & inspection
- Fills domestic bumping capabilities gaps for unique USG/DoD needs
- State-of-the-art 2D/3D automated optical inspection
- Capacity & automation expansion currently underway



NGMC Wafer-Level Packaging

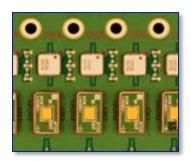


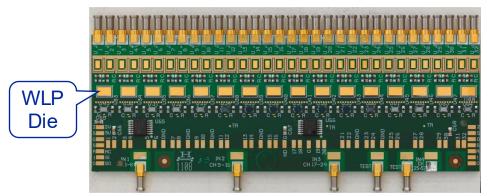


Wafer-Level Packaging Applications

Direct Integration to Board







Multiple Technology Integration



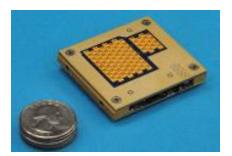


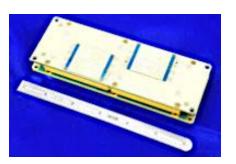
ABCS HEMT LNA

InP HBT PA & control

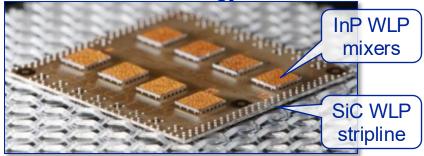
GaAs HEMT PS & Switches

Tiled Phased Arrays

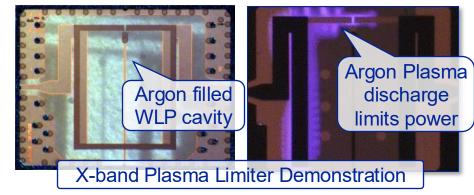




High Q Passive Technology



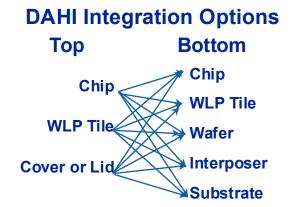
Microscale Plasma Devices





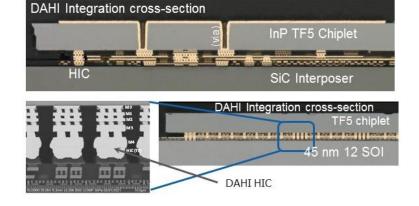
NGMC 3D Heterogeneous Integration

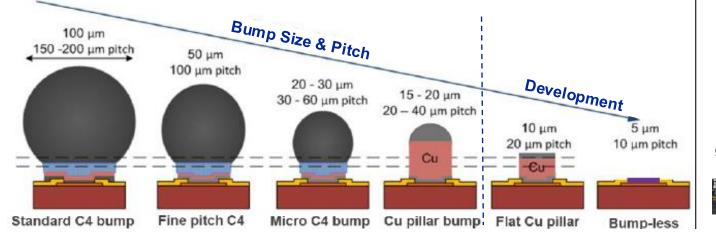
DAHI Integration Process DARPA Compound Semiconductor (CS) Chiplet Multiple CS (InP, GaAs, GaN, etc.) Multiple CS Chiplets integration technologies passes **Dense** Different interconnects chiplet sizes **Base wafer (Interposer, CS or CMOS)**



Au/Au bond

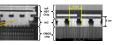
- No dendritic or whisker formation
- High thermal conductivity
- Low thermal resistance
- Malleable/ductile (as opposed to brittle solder compounds)





Au-Au DAHI HIC

9um, 15 um pitch



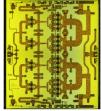
DAHI HIC

Heterogeneous integration (HI) technology enables lower cost, reduced size and weight, and enables higher performance

NGMC Product Marketing Product Data Sheets and Web Presence







x=3.30mm; y=4.10 mm

Product Features

- RF frequency: 27 to 31 GHz
- Linear Gain: 20 dB typ.
- Psat: 45.6 dBm typ.
- Die Size: 13.52 sq. mm.
- 0.2um GaN HEMT Process
- 4 mil SiC substrate

HTS (Schedule B) code: 8542.33.0000

■ DC Power: 28 VDC @ 2.64 A

Applications

- Point-to-Point Digital Radios
- Point-to-Multipoint Digital Radios
- SatCom Terminals

Product Description

The APN311 monolithic GaN HEMT amplifier is a broadband, two-stage power device, designed for use in Ka-Band communication applications such as SatCom Terminals and point-to-point and point-to-multipoint digital radios. To ensure rugged and reliable operation, HEMT devices are fully passivated. Both bond pad and backside metallization are Au-based that is compatible with epoxy and eutectic die attach methods.

Performance Characteristics (Ta = 25°C)

Min	Тур	Max	Unit
27		31	GHz
19	20		dB
5	12		dB
>5	10		dB
	43		dBm
44	45		dBm
	30		%
30			%
20		28	V
	-3.56		V
	-3.46		V
	830		mA
	1860		mA
	27 19 5 >5 >5	27 19 20 5 12 >5 10 44 45 30 30 20 -3.56 -3.46 830	27 31 31 19 20 5 12 >5 10 43 44 45 30 30 28 -3.56 -3.46 830

^{*} Pulsed-Power On-Wafer unless otherwise noted

Preliminary Information: The data contained in this document describes new products in the sampling or preproduction phase of development and is for information only. Northrop Grumman reserves the right to change without notice the characteristic data and other specifications as they apply to this product. The product represented by this datasheet is subject to U.S. Export Law as contained the EAR regulations.

10/5/2020 Web: http://www.as.northropgumman.com/mps @2020 Northrop Grumman Systems Corporation Phone: (310) 814-5000 · Fax: (310) 812-7011 · E-mail: as-mps sales@ngc.com

Website Links
YesWeGaN.com

OVERVIEW

Power Amplifiers

Part	Description	Frequency (GHz)	Gain (dB)	P1dB (dBm)	PSat (dBm)	Availability
APH667	GaAs HEMT High Power Amplifier	81 - 86	17	TBD	25.5	Stock
APH668	GoAs HEMT High Power Amplifier	71 - 76	19	TBD	28	Stock
APH66P	GaAs HEMT Medium Power Amplifier	81 - 86	16	20	23.5	Stock .
APH670	GaAs HEMT Medium Power Amplifier	71 - 76	21	TBD	25	Stock

Before handling, assembling or testing our GaAs MMICs please review our "GaAs IC Die Handling, Assembly and Testing Techniques" application note

Power Amplifiers

Port	Description	(GHz)	Gain (dB)	P1dB (dBm)	Pacif(dEm)	Availability
APH482	HEMT High Power Amplifier	92-96	7.5	22	25	Stock
APH484	HEMT High Power Amplifier	93 - 95	10	21	23	Stock
APH631	HEMT Power Amplifier	92 - 96	23	15	18	Stock
APH635	HEMT Power Amplifier	92 - 95	17	20	22	Stock

GaN Power Amplifiers Products

Port	Description	Frequency (GHz)	Gain (dB)	P1dB (dBm)	Pacit (dBm)	Form	Availability
APN2d3	GaN HEMT Power Amplifier	23-28	20	38	40.5	Dte	Stock
APN244	GaN HEMT Power Amplifier	23-28	21	37	39	Dte	Stock
APN228	GaN HEMT Power Amplifier	27-32	19.5	39	41.2	Die	Stock
APN229	GaN HEMT Power Amplifier	27-32	20	17	39	Die	Stock
APN248	GaN HEMT Power Amplifier	27-31	17.5	42	44	Die	Stock
APN292	GaN HEMT Power Amplifier	27-31	20	42	42	Dte	Stock
APN311	GaN HEMT Power Amplifier	27-31	20	43	45	Dte	Stock
APN173	GaN HEMT Power Amplifier	34-36	19.5	TBD	37.5	Dte	Stock
APN236	GaN HEMT Power Amplifier	34.5-35.5	16	38	40	Dte	Stock
APN167	GaN HEMT Power Amplifier	43-46	20	35.5	38.5	Die	Stock
APN318	GaN HEMT Power/Driver Amplifier	472-51.4	15	-	40	Die	Pre- Production
APN319	GaN HEMT Power/Driver Amplifier	47:2-51.4	16	-	37	Die	Pre- Production

Part	Description	Frequency (GHz)	Gain (dB)	Output Power (dBm) P1dB
APN267	GaN HEMT Distributed Amplifier	2 to 18	10	35
APN270	GaN HEMT Power Amplifier	9 to 13.2	13	39
APN252	GaN HEMT Power Amplifier	10 to 14	25.5	34
APN250	GaN HEMT Driver Amplifier	10 to 14	13	39
APN226	GaN HEMT Power Amplifier	13 to 16	20	36
APN232	GaN HEMT Power Amplifier	13.5 to 15.5	13	38.5
APN237	GaN HEMT Dual Channel Power Amplifier	13.5 to 15.5	12.5	40.5
APN293	GaN HEMT Power Amplifier	16 to 20.5	10	36.5

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NGMC Silicon Technology – Baltimore

Technology	Status	Supply Voltage (V) Digital/Analog	Applications	Description	PDK
L-Band	Production	NA/50	Power transistors for pulsed radar/communications	Silicon NPN Microwave Power	Yes
S-Band	Production	NA/45	100 krads	Power transistors for pulsed radar/communications	Yes
CCD	Production	10/12	Visible image sensor	Indium Tin Oxide CCDs with 4µm CMOS 8V CCD Input Drive	Yes
PCBLVRH	Production	10/10	Smart power control (regulators, fault isolators)	Rad Hard 10V BiCMOS No PNP transistors	Yes
PCBMVRH	Production	15/15	Smart power control (regulators, fault isolators)	Rad Hard 15V BiCMOS No PNP transistors	Yes
PCB60	Production	15/60	Regulators for GaN devices	60V BiCMOS	Yes
PCB40RH	Pre-Production	15/40	Analog multiplexers, analog switches	100K Rad Hard 40V CMOS and Trench Isolation	Yes
CMS8	Production	5/5	SONOS FPGAs, CMOS ASICs, 1M EEPROM	Rad Hard 0.8µm CMOS with Nonvolatile Memory	Yes
CMS5	Production	3.3/5	CMOS ASICs	Rad Hard 0.5µm CMOS	Yes

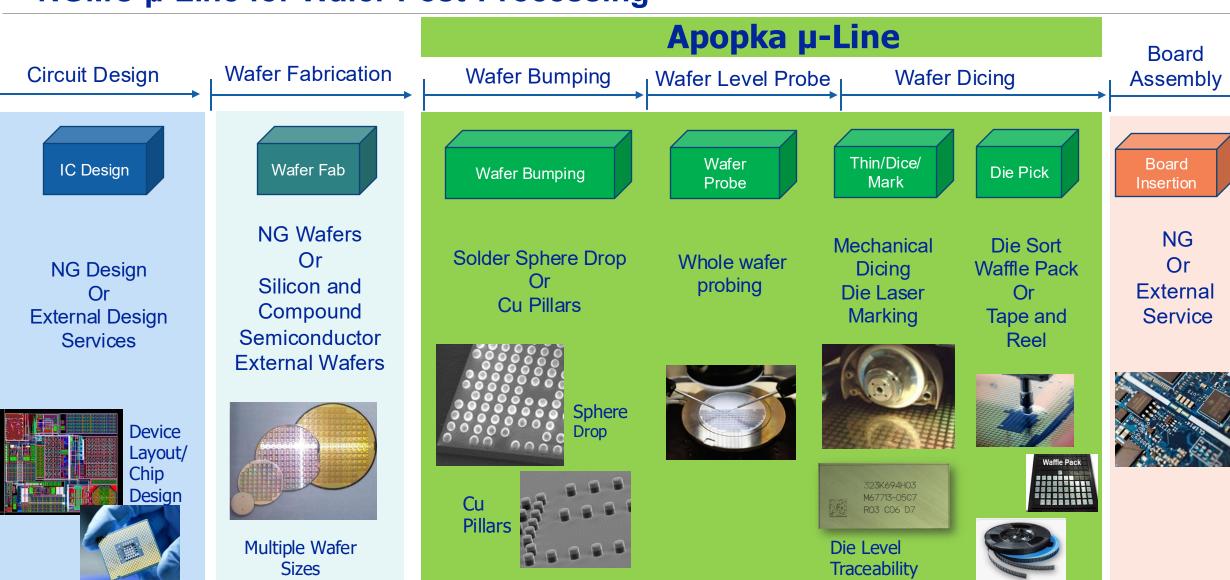


NGMC Compound Technology – Baltimore

Technology	Status	Operating Voltage (V)	Applications	Description	PDK
SiC	Production	100	Power transistors for pulsed radar/communications	Discrete Silicon Carbide Power Transistors	Yes
GaAs HBT	Production	8	Upconverter, Downconverter, LO	Low phase noise HBT	Yes
SLCFET 3S	Production	-14	Switches, Switch Couplers, Switch Matrices, Reconfigurable Filters	Low-Loss, High Linearity RF Switch, RWOH available	Yes
SLCFET 3HP	Production	-20	Switches, Switch Couplers, Switch Matrices, Reconfigurable Filters	High Power, Low-Loss RF Switch, RWOH available	Yes
SLCFET Diode	Development	-14	Level Shifters for Receiver Protector, Limiters	Diode Integrated with SLCFET Switch	Yes
SLCFET Amplifier	Development	2.5 - 15	Power Amplifiers, LNAs, T/R MMICs	VHF - Ka band Superlattice Amplifier	Yes



NGMC μ-Line for Wafer Post-Processing





NORTHROP GRUMMAN

Solder Sphere Drop Specifications

Parameter	300 μm Eutectic	200 μm Eutectic	200 μm High Lead	175 μm High Lead	80 μm Pb Free
Solder Alloy	Sn63Pb37	Sn63Pb37	Pb90Sn10	Pb90Sn10	SAC305
Passivation Thickness	5 μm	5 μm	5 μm	4 µm	5 μm
Polyimide Opening	180 µm	110 µm	110 µm	95 µm	61 µm
UBM Diameter	280 μm	180 µm	180 µm	146 µm	81 µm
UBM Thickness	5 μm	5 μm	5 µm	5 μm	5 μm
Average Height	235 μm	160 µm	160 μm	145 µm	60 µm
Average Diameter	320 μm	205 μm	205 μm	180 µm	85 µm
Shear	>283 gf	>90 gf	>90 gf	>60 gf	>8 gf
Recommended Pitch	500 μm	375 μm	375 μm	300 µm	150 μm
Wafer Size Capability	6", 8" & 12"	6", 8" & 12"	6", 8" & 12"	6", 8" & 12"	6", 8" & 12"
Wafer Size/Alloy Production Ready	8"	8"	8"	8"	8"



NGMS Apopka μ-Line

Solder Sphere Drop Specifications

Wafer Size	Solder Sphere Drop	Cu Pillar	Plated Bump
100 mm	N/A	Development	Capability
150 mm	Capability	Capability	Capability
200 mm	Production	Capability	Capability
300 mm	Development	Capability	Capability

Alloy	Solder Sphere Drop	Cu Pillar	Plated Bump
Eutectic Sn63/Pb37	Production	Development	Development
Hi Lead Pb90/10	Production	Capability	Capability
SAC305	Capability	N/A	N/A
SnAg	Capability	Capability	Capability

Solder Sphere Size	Solder Sphere Drop	
80 μm	Production	
100 μm	Production	
175 µm	Production	
200 μm	Production	
300 μm	Production	



Microelectronics Products & Services (MPS)

NGMC External Sales Operating Unit

- MPS is NGMC's storefront for external access to microelectronics and microelectronic-based products
- Product portfolio includes ASIC sales, MMIC sales, foundry services, design and test services, wafer bumping, IC packaging and RF units
- Leverages NGMC microelectronics foundry technologies utilized in NGC space, airborne and ground products
- MPS business operates under fixed price contracts, commercial terms
 - –Other Contract approaches are available for development & maturation
- All NGMC Sites are AS9100 & DMEA Accreditation of Trust



Defense Industrial Base

Provide discrimination to national asset systems



Commercial Market

Provide cost effective, high performing solutions





NGMC's Offering

- In 2024, Northrop Grumman has opened access to its Microelectronics capabilities and capacity
 to both the defense industrial base and commercial. To support this thrust, NG has re-organized
 our Microelectronics Sites, staff & capabilities into the Northrop Grumman Microelectronic Center
 (NGMC).
- NG's intention is to continue to address Microelectronics gaps which the domestic commercial microelectronic industry hasn't
- Value to you is a Microelectronics partner who understand performance and quality impact to USG systems along with the criticality of speed
- We have systems & controls in place to protect your IP and proprietary information
- Next Steps: Provide engagement opportunities with your Product Architects and Chief Engineers

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